




ANDHRA LOYOLA INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTU Kakinada)

Accredited by NAAC & An ISO 9001:2015 Certified Institution

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FACULTY PROFILE

Name of the Faculty	Mr L.Karunakar		
Designation	Assistant professor		
Department	EEE	Date of Birth 20/08/1987	
Date of Joining the Institution	06-06-2013	Native place VIJAYAWADA A.P, India	
Academic Qualification with Class/Grade	PG: M.Tech 1st Class with DISTINCTION UG: B.Tech 1st Class		
Employee ID	ALIET-13-10		
E-mail / Mobile	lankakarunakar@aliet.ac.in 8919168220,9030104738		
Total Experience in Years	Teaching: 7 years	industry: 6 months	
Papers Published in Journals -	UGC – 11		
Faculty Development Programmes attended:	1		
Papers presented in Conferences/ attended		Workshops: 4	
Projects Guided		Projects at UG Level: - 17	
Professional Memberships - 1	MISTE		
Board of Studies Member:	<ul style="list-style-type: none">Maris Stella College Vijayawada, Andhra Pradesh.		

Subjects handled for B. Tech 2013 to present	<ul style="list-style-type: none"> • Power systems-II • PSOC • BEEE • NA • NCES • EM-II • CS • SEM • Power systems lab • Electrical simulation lab • Control systems lab • EMS lab • EM-I lab
Conferences/ Seminars / Workshops / FDP Organised @ ALIET	<ul style="list-style-type: none"> • Act as a coordinator for 1-NCEE 2K16 • Act as a coordinator for 2-NCEE 2K17 • Act as a coordinator for 3-NCEE 2K18 • Act as a coordinator for 4-NCEE 2K19 • Act as a coordinator for 3 DAY FDP 2K19
PAPER PUBLICATIONS	
<p>1. Published a paper titled "Proposed structure of fault current limiter with power quality improvement" International Journal of Scientific & Engineering Research in 2012</p> <p>2. Published a paper titled "Thyristor Based Fault Current Limiter to Control the Magnitudes of Faults" International Journal of modern Engineering Research in 2013</p> <p>3. Published a paper titled "Comparison of simulation analysis of 100kw grid connected pv system" In IJRECE in 2016</p> <p>4. Published a paper titled "Bridgeless SEPIC Rectifier with Unity Power Factor And Reduced Conduction Losses" In IJRECE in 2016</p> <p>5. Published a paper titled "Enhancement of Power Quality using ELC from Isolated power Generation" in IJMTST in 2017</p> <p>6. Published a paper titled "power factor correction using step up chopper fed to a DC motor drive" in IJMTST in 2017</p> <p>7. Published a paper titled "Under Ground Cable Fault Detection By Using Arduino" in IJMTST in 2018</p> <p>8. Published a paper titled "Line Follower Based Smart Car" In IJMTST in 2018</p> <p>9. Published a paper titled "CLOSED LOOP CONTROL OF DC-DC CONVERTER WITH HYBRID INPUT SOURCES" in RECENT ADVANCES IN CONTROL OF ENERGY SYSTEMS in 2016</p> <p>10. published paper titled "smart phone controlled metal detector vehicle with GSM and arduino interfacing" in march 2019</p> <p>11. published paper titled "power quality improvement by controlling fault currents using NSFCL" in march 2019</p>	