ANDHRA LOYOLA INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved by AICTE, Recognized by Govt of AP, Affiliated to JNTUK, Kakinada)
An ISO 9001: 2008 Certified Institution

Govt Polytechnic Post, ITI Road, ALC Campus, Near Ramesh Hospital, Vijayawada-8

FACULTY PROFILE

Name of the Faculty	Mrs.V.ANANTHA LAKS	НМІ	A STATE OF THE STA	
Designation	Assistant Professor	Assistant Professor		
Department	Electrical and Electronics Engineering			
Date of Joining the Institution	18-09-2008	18-09-2008		
	UG: B.Tech 1 st Class with distinction			
Qualification with Class/Grade	PG: M.Tech 1 st Class with distinction			
	Ph.D. (Pursuing at Andhta University, Visakhapatnam)			
Employee ID	ALIET-08-06	ALIET-08-06		
E-Mail	anusark2001@yahoo.co.in	anusark2001@yahoo.co.in		
Total Experience in Years	Teaching: 15years	Industry: 0 years	Research:00	
Papers Published	National: 00	International: 02		
Papers presented in Conferences	National: 06	International: 02		
PhD Guide? Give field & University	Field: Microgrid Guide:prof.T.R.Jyothsna	University: Andhra University		
PhDs / Projects Guided	PhDs: 00	Projects at Masters Level: 00 Projects at UG Level: 15		
Books Published/IPRs/Patents				
Professional Memberships	LMISTE, ISRD			
Consultancy Activities				
Awards				
Grants fetched				
Whether Ratified by University (Yes/No)				

Any Other Achievements:

Experience in other Institutions:

- 1. Worked as Asst.prof. at EEE Dept. Of SVECW, Bhimavaram from june 6th 2003 to 1st May 2006.
- 2. Worked as Asst.prof. at EEE Dept. Of LBRCE, Mylavaram from 2nd May 2006 to 17th Sep 2008.