




ANDHRA LOYOLA INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTU Kakinada)

Accredited by NAAC & An ISO 9001:2015 Certified Institution

ITI Road, ALC Campus, VIJAYAWADA - 520 008 :: Website : www.aliet.ac.in :: Ph : 0866 - 2476161

FACULTY PROFILE

Name of the Faculty	Mr.RAVI.GORAPUDI		
Designation	Assistant Professor		
Department	ELECTRONICS AND COMMUNICATION ENGINEERING	Date of Birth 23/04/1985	
Date of Joining the Institution	02-07-2012	Native place KHAMMAM, TELANGANA, India	
Academic Qualification with Class/Grade	<ul style="list-style-type: none">PG : M.Tech (ECE) First Class with Distinction from JNTUH.UG : B.Tech (ECE) First Class from KU.		
Professional Qualification	<ul style="list-style-type: none">Qualified in FET Conducted by JNTUHCompleted FACULTY INDUSTRIAL TRAINING Programme from 06-04-2018 to 09-05-2018 at Effectronics systems pvt ltd.vijayawada.Course completion on BASICS OF PLC by SIEMENS in sidhartha engineering college		
Employee ID	ALIET-12-15		
E-mail / Mobile	ravigorapudi234@gmail.com +91 9848794818		
Total Experience in Years	Teaching: 10 years	Industry 00	Research : 00
Papers Published in Journals -	UGC – 5 A Peer- Reviewed- 2	Articles in Books: 0	

Faculty Development Programmes attended:	11	Guest Lectures Delivered: 00
Papers presented in Conferences/ attended	National: 0	International: 0 / Workshops: 08
Are u a PhD Guide? If Yes, Give field & University	Field: ---	
PhDs / Projects Guided	PhDs : 00	Projects at PG Level : 01
	MPhil -00	Projects at UG Level: - 15
Books Published/IPRs/Patents	NIL	
Professional Memberships	1.Associate Member of Institute of Engineers 2.Member of Indian Society of Systems for Science &Engineering	
Editorial Board Membership	NIL	
Board of Studies Member:	NO	
Foreign Projects	NIL	
National Project	NIL	
Grants fetched	00	
Awards - 01	<ul style="list-style-type: none">• Ratified by jntuh,jntuk	
Conferences/ Seminars / Workshops / FDP Organised @ ALIET	<ul style="list-style-type: none">• PCB Design workshop• BSNL Workshop• Seminar on electronic circuit design•	
Any Other Achievements	✓ Completed the course in B.Th	
Other Responsibilities	<ul style="list-style-type: none">• Exam cell in-charge• VLSI Research group coordinator• Alumni co-ordinator• IETE co-ordinator• EDC Lab in-charge	
Whether Ratified by University (Yes/No)	YES	
Faculty Development Programmes attended: 2019-20		
1. Participated in a two day online FDP on “Micro strip antennas for advanced wireless communications”, organized by KHIT, from 15th to 16th May 2020.		
2. Participated in a four day online FDP on “Digital image processing using MATLAB “, organized by skilltohire, from 24th to 27th April 2020.		
3.Participated in 3 day faculty development programme by north storm Mumbai.		
4.participated a five day FDP on arduino by satyabhama university from 04-05-2020 to 08-05-2020.		
5. Participated in a three day national level online FDP on “COVID-19 as global crisis: application and		

appreciation of language and literature “, organized by ALIET, from 12th to 14th May 2020.

6.certificate of appreciation on completion of NAAC Awareness programme organized by MMIT From 08-05-2020 to 14-05-2020.

7. Participated in a five day national level online FDP on “modern trends in Communication, signal processing and VLSI/MEMS”, organized by ALIET, from 19th to 23rd May 2020.

8.Participated in a two week summer faculty development programme on important engineering subjects in JNTUK.Kakinada from 02-05-2019 to 07-05-2019 .

9. Participated in a two week summer faculty development programme on important engineering subjects in JNTUK.Kakinada from 09-05-2019 to 14-05-2019 .

10. Participated in a two week faculty development programme on Mixed signal design in VVIT from 04-11-2019 to 15-11-2019

11. . Participated in a two week faculty development programme on Mixed signal design in VVIT from 04-11-2019 to 15-11-2019

Online Webinars Attended: 2019-20

1.Participated webinar on the need for change in pedagogy and a simple way to teach online with a smart phone for beginners conducted by USHA RAMA ENGINEERING COLLEGE

2.participated in awareness programme in intellectual property rights by chatrapati sivajiraje college of engineering on 14-5-2020.

3.participated a webinar on digital marketing by sathyabhama university on 08-05-2020.

4.participated a webinar on **grid connected and stand alone micro grid design** in ERODE SENGUNTHAR college of engineering.

5.participated on **virtual classroom management using teams** in lakkireddy balireddy college of engineering.

6.

Online Quiz Programme attended: 2019-20

1.CERTIFICATE OF APPRECIATION in Quiz on recent trends in IT ,conducted by shree L.R.Tiwari engineering college on 09-05-2020

2. CERTIFICATE OF APPRECIATION on **faculty programme on NBA** by **bharathi vidyapeet college of engineering**

3. CERTIFICATE OF APPRECIATION on **basics of computers on 9-5-20** in **shree l.r tiwari college**

of engineering
Post-Doctoral Publication / Seminars/Workshops/Guest Lectures:
1.
List of Publications in Journals
<ol style="list-style-type: none"> 1. Published a paper with “FPGA implementation of Multi ordered FIR filters with low complexity “ as title in discovery journal,2015 2.published a paper on product label,obstacle and sign boards detection for visually impaired people in IJARI IN 2020 3. Published a paper with “DESIGN AND IMPLEMENTATION OF PESTICIDE SPRAYING ROBOT USING IOT” in IJARI in 2020. 4. Published a paper with reconfigurable optimised FPGA Design with low hardware overhead in IJIRAT in 2016. 5. Implementation of self service medicine vending machine in rural areas in IJCRT in 2018. 6. Published a paper on Smart navigation assist for visually impaired and differently abled in IJCRT. In 2018. 7. published a paper on”An intelligent detection notification to avoid potholes on road to aid drivers “in IJRECE in 2019. 8. published a paper on “Design and implementation of embedded home surveillance system using arduino and node MCU “in IJRECE in 2019. 9.Removal and reconstruction of shadow in hifh resolution images in IJART in 2017
List of Publications (A Peer-Reviewed Journal)
1.
Articles published in Book: NILL
1.

National Seminars:NILL
1.
International Seminars:NILL
1.
Workshops
<p>1. Participated in the two day workshop “NI-Labview”, ALIET, Vijayawada, from 19th to 22nd September, 2016.</p> <p>2. Participated in the two day workshop “System Design Using Xilinx FPGAs”, ALIET, Vijayawada, from 27th to 28th June, 2013.</p> <p>3..Organised PCB Design workshop for students in 2020,2019,2018</p> <p>4.Certificate of appreciation in PCB DESIGN workshop in 2019</p> <p>5.co-ordinated BSNL workshop in 2015</p> <p>6.Participated workshop on design and simulation of antenna in shri Vishnu engineering college,bheemavaram from03-10-2016 to 07-10-2016</p> <p>7.Participated in the two day workshop “System Design Using Xilinx FPGAs”, ALIET, Vijayawada, from 27th to 28th June, 2013.</p> <p>8. 3.organised a two day workshop “NI-Labview”, ALIET, Vijayawada, from 19th to 22nd September, 2016.</p>
Guest Lectures
1.
Prizes
1.
Social Activities
<p>1.ECO CLUB</p> <p>2.SWACHA BHARAT</p> <p>3.</p>
Events Organized

1.Organised BSNL Workshop for students

2.Organised PCB Design workshop for students in 2020,2019,2018